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EXAMINING GROUP 2112

PATENT

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Margaret A. Sullivan  
Signature

Appl No. : 09/637,846 Confirmation No. 7094  
Applicant : Sujith K. Arramreddy, et al.  
Filed : August 10, 2000  
Title : PERIPHERAL COMPONENT INTERCONNECT ARBITER  
IMPLEMENTATION WITH DYNAMIC PRIORITY SCHEME  
TC/A.U. : 2112  
Examiner : Kim T. Huynh  
Docket No. : 46884/RJP/B600  
Customer No. : 23363

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**AMENDMENT FILED WITH REQUEST FOR CONTINUING EXAMINATION (RCE)**

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Post Office Box 7068  
Pasadena, CA 91109-7068  
February 20, 2004

Commissioner:

In response to the Office Action mailed November 20, 2003 and the Advisory Action mailed January 29, 2004, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.